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## Abstract

A system and method for driving a flat panel display are provided. The system includes a register connected to a latch having outputs connected to logic circuits which correspond to the electrodes. Each logic circuit is also connected to the register, and generates control signals based on the next state and the current state of the corresponding electrode. Each logic circuit is configured such that upon an activation signal, the logic circuit control signals connect the change up driver to electrodes having a low current state and a high next state, and connect the change down driver to electrodes having a high current state and a low next state.